

DATA SHEET

HIGH VOLTAGE LEAD FREE CHIP RESISTORS

RV_P series 0.5%, 1%, 5%

sizes 0603/0805/1206/2010/2512 RoHS compliant

IEC 62368-1 Safety Certificate issued by UL Demko: sizes 0603/0805/1206



YAGEO



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SCOPE

This specification describes RV0603/0805/1206/2010/2512 high voltage chip resistors made by thick film process.

APPLICATIONS

- Total lead free without RoHS exemption
- Converter
- Printer equipment
- Battery charger
- Computer
- Power supply

FEATURES

- RoHS compliant
- Reducing environmentally hazardous wastes
- High component and equipment reliability
- Non-forbidden materials used in products/production
- Halogen Free Epoxy
- Moisture sensitivity level: MSL I
- IEC 62368-1: 2018 safety certificate issued by UL Demko for the following sizes and resistance ranges:

- 0603: $I00K\Omega$ to $I2M\Omega$

- 0805: I00K Ω to 24M Ω

- I206: I00K Ω to 27M Ω

* Please refer to UL certification

* Waiting for establishing

ORDERING INFORMATION - GLOBAL PART NUMBER

Both part numbers are identified by the series, size, tolerance, packing type, temperature coefficient, taping reel and resistance value.

YAGEO BRAND ordering code

GLOBAL PART NUMBER (PREFERRED)

RV XXXX X X X XX XXXX P

(1) (2) (3) (4) (5) (6) (7

(I) SIZE

0603/0805/1206/2010/2512

(2) TOLERANCE

 $D = \pm 0.5\%$

 $F = \pm 1\%$

 $J = \pm 5\%$

(3) PACKAGING TYPE

R = Paper/PE taping reel

K = Embossed taping reel

(4) TEMPERATURE COEFFICIENT OF RESISTANCE

- = Base on spec

(5) TAPING REEL

07= 7 inch dia, Reel

(6) RESISTANCE VALUE

There are $2\sim4$ digits indicated the resistor value. Letter R/K/M is decimal point, no need to mention the last zero after R/K/M, e.g. I K2, not I K20.

Detailed resistance rules show in table of "Resistance rule of global part number".

(7) DEFAULT CODE

Letter P is lead free (without RoHS exemption)

Resistance rule of global part number

Resistance code ru	le Example
XXKX	10K = 10,000 Ω
(10 to 97.6 KΩ)	97K6 = 97,600 Ω
XXXK	$100K = 100,000\Omega$
(100 to 976 KΩ)	$976K = 976,000\Omega$
XMXX	$IM = 1,000,000 \Omega$
(1 to 9.76 M Ω)	$9M76 = 9,760,000 \Omega$
XXMX	$10M = 10,000,000 \Omega$
(10 to 16 M Ω)	$27M = 27,000,000 \Omega$

ORDERING EXAMPLE

The ordering code of a RV1206 chip resistor, value I $M\Omega$ with $\pm 5\%$ tolerance, supplied in 7-inch tape reel is: RV1206JR-071MP.

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MARKING

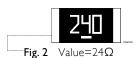
RV0603/0805/1206/2010/2512



E-24 series: 3 digits, ±5%

First two digits for significant figure and 3rd digit for number of zeros

RV0603



E-24 series: 3 digits, $\pm 0.5\%$ & $\pm 1\%$

Exception values 10/11/13/15/20/75 of E24 series

One short bar under marking letter



E-96 series: 3 digits, $\pm 0.5\% \& \pm 1\%$

Including values 10/11/13/15/20/75 of E24 series

First two digits for E-96 marking rule and 3rd letter for number of zeros

RV0805/1206/2010/2512



Both E-24 and E-96 series: 4 digits, $\pm 0.5\%$ & $\pm 1\%$

First three digits for significant figure and 4th digit for number of zeros

For further marking information, please refer to data sheet "Chip resistors marking".

<u>CONSTRUCTION</u>

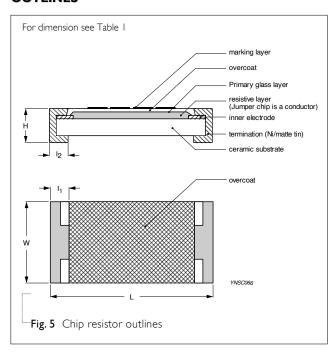
The resistor is constructed on top of a high-grade ceramic body. Internal metal electrodes are added on each end to make the contacts to the thick film resistive element. The composition of the resistive element is a noble metal imbedded into a glass and covered by a second glass to prevent environment influences. The resistor is laser trimmed to the rated resistance value. The resistor is covered with a protective epoxy coat, finally the two external terminations (matte tin on Nibarrier) are added. See fig.5

DIMENSIONS

Table I For outlines see fig. 5

TYPE	L (mm)	W (mm)	H (mm)	I _I (mm)	l ₂ (mm)
RV0603	1.60 ±0.10	0.80 ±0.10	0.45 ±0.10	0.25 ±0.15	0.25 ±0.15
RV0805	2.00 ±0.10	1.25 ±0.10	0.50 ±0.10	0.35 ±0.20	0.35 ±0.20
RV1206	3.10 ±0.10	1.60 ±0.10	0.55 ±0.10	0.45 ±0.20	0.45 ±0.20
RV2010	5.00 ±0.10	2.50 ±0.15	0.55 ±0.10	0.55 ±0.15	0.55 ±0.20
RV2512	6.35 ±0.10	3.10 ±0.15	0.55 ±0.10	0.60 ±0.20	0.60 ±0.20

OUTLINES





ELECTRICAL CHARACTERISTICS

Table 2

		CHARACTERISTICS					
TYPE	RESISTANCE RANGE	Rated Power	Operating Temperature Range	Max. Working Voltage	Max. Overload Voltage	Dielectric Withstanding Voltage	Temperature Coefficient of Resistance
RV0603	5% (E-24) 47 Ω to 10M Ω 1% (E-24/E-96) 47 Ω to 10M Ω 0.5% (E-24/E-96) 47 Ω to 10M Ω	1/10W		350V	500V	500V	
RV0805	5% (E-24) 47 Ω to 22M Ω 1% (E-24/E-96) 47 Ω to 22M Ω 0.5% (E-24/E-96) 47 Ω to 10M Ω	1/8 W		400 V	800 V	800 V	
RV1206	5% (E-24) 47Ω to $27M\Omega$ I% (E-24/E-96) 47Ω to $27M\Omega$ 0.5% (E-24/E-96) 47Ω to $15M\Omega$	1/4 W	−55 °C to +155 °C	500 V	1,000 V	I,000 V	±200 ppm/°C
RV2010	5% (E-24) 47 Ω to 22M Ω 1% (E-24/E-96) 47 Ω to 22M Ω 0.5% (E-24/E-96) 47 Ω to 10M Ω	3/4W		500 V	1,000 V	1,000 ∨	
RV2512	5% (E-24) 47 Ω to 16M Ω 1% (E-24/E-96) 47 Ω to 16M Ω 0.5% (E-24/E-96) 47 Ω to 10M Ω	ΙW	_	500 V	1,000 V	I,000 V	

FOOTPRINT AND SOLDERING PROFILES

For recommended footprint and soldering profiles, please refer to data sheet "Chip resistors mounting".

PACKING STYLE AND PACKAGING QUANTITY

Table 3 Packing style and packaging quantity

PACKING STYLE	reel Dimension	RV0603	RV0805	RV1206	RV2010	RV2512
Paper/PE taping reel (R)	7" (178 mm)	5,000	5,000	5,000		
Embossed taping reel (K)	7" (178 mm)				4,000	4,000

NOTE

1. For Paper/PE/Embossed tape and reel specification/dimensions, please refer to data sheet "Chip resistors packing".

MLB206 1

FUNCTIONAL DESCRIPTION

OPERATING TEMPERATURE RANGE

Range: -55 °C to +155 °C

POWER RATING

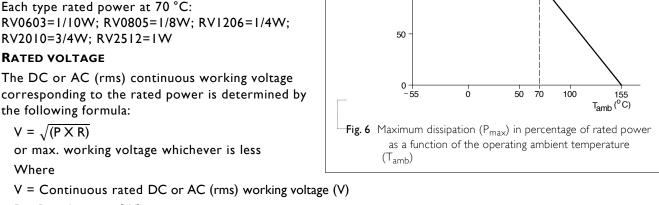
Each type rated power at 70 °C:

corresponding to the rated power is determined by the following formula:

$$V = \sqrt{(P \times R)}$$

P = Rated power (W)

 $R = Resistance value (\Omega)$



Р

(%P_{rated}) 100

Maximum working voltage can be applicable to resistors only if the resistance value is equal to or higher than the critical resistance value.

TESTS AND REQUIREMENTS

Table 4 Test condition, procedure and requirements

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS	
Life/ Operational Life/ Endurance	MIL-STD-202G-method 108A IEC 60115-1 7.1	1,000 hours at 70 ± 5 °C applied RCWV 1.5 hours on, 0.5 hour off, still air required	±(2%+0.05 Ω)	
High Temperature Exposure/ Endurance at upper category temperature	MIL-STD-202G-method 108A IEC 60115-1 4.25.3	I,000 hours at maximum operating temperature depending on specification, unpowered No direct impingement of forced air to the parts Tolerances: I55±3 °C	±(1%+0.05 Ω)	
Moisture Resistance	MIL-STD-202G-method 106	Each temperature / humidity cycle is defined at 8 hours, 3 cycles / 24 hours for 10d with 25 °C / 65 °C 95% R.H, without steps 7a & 7b, unpowered Parts mounted on test-boards, without condensation on parts	±(2%+0.05 Ω)	
		Measurement at 24±2 hours after test conclusion		
Thermal Shock	MIL-STD-202G-method 107G	-55/+125 °C	$\pm (0.5\% + 0.05~\Omega)$ for 10 K Ω to 10 M Ω	
		Note: Number of cycles required is 300. Maximum transfer time is 20 seconds. Dwell time is 15 minutes. Air — Air	\pm (1%+0.05 Ω) for others	
Short time overload	IEC60115-1 8.1	2.5 times RCWV or maximum overload voltage	±(2%+0.05 Ω)	
Overload		whichever is less for 5 sec at room temperature	No visible damage	
Board Flex/	IEC60115-1 9.8	Device mounted on PCB test board as described,	±(1%+0.05 Ω)	
Bending		only I board bending required Bending for 0603 & 0805: 3mm I 206 & above: 2mm	No visible damage	
		Holding time: minimum 60 seconds		
		Ohmic value checked during bending		
Humidity	IEC 60115-1 10.4	Steady state for 1,000 hours at 40°C / 95% R.H RCWV applied for 1.5 hours on and 0.5 hour off	± (3.0%+0.05 Ω)	
		TOVV V applied for 1.5 flours off and 0.5 flour off		





Chip Resistor Surface Mount RV_P SERIES 0603/0805/1206/2010/2512 (RoHS Compliant)

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Solderability - Wetting	IPC/JEDECJ-STD-002B test B IEC 60068-2-58	Electrical Test not required Magnification 50X SMD conditions: Ist step: method B, aging 4 hours at 155 °C dry heat 2nd step: leadfree solder bath at 245±3 °C Dipping time: 3±0.5 seconds	Well tinned (≥95% covered) No visible damage
- Leaching	IPC/JEDECJ-STD-002B test D IEC 60068-2-58	Leadfree solder, 260 °C, 30 seconds immersion time	No visible damage
- Resistance to MIL-STD-202G-method 210F Soldering Heat IEC 60068-2-58		Condition B, no pre-heat of samples Leadfree solder, 260 °C, 10 seconds immersion time Procedure 2 for SMD: devices fluxed and cleaned with isopropanol	$\pm (1\% + 0.05 \ \Omega)$ No visible damage



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Chip Resistor Surface Mount RV_P SERIES 0603/0805/1206/2010/2512 (RoHS Compliant)

REVISION HISTORY

REVISION DATE CHANGE NOTIFICATION DESCRIPTION

Version 0 May. 10, 2022 - - First issue of this specification

Chip Resistor Surface Mount

RV_P

SERIES

0603/0805/1206/2010/2512 (RoHS Compliant)

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